

ESLERATE

Making fast *faster*.

Drive out cost, complexity, and development time with *ESLerate™*, the industry's only electronic system-level (ESL) design automation tool that provides a path for translating general-purpose processor binary and assembly code into RTL VHDL and Verilog hardware accelerators for SoC FPGA platforms.

The growing design complexity, resources, and performance constraints for real-time applications are creating unprecedented system development challenges that are increasingly difficult to support with fragmented tool flows and limited budgets. Binachip's *ESLerate™* provides a comprehensive solutions for today's embedded systems developers to achieve optimal performance through hardware and software trade-offs while reducing design times *from months to hours*.

Discover how *ESLerate™* can help you meet your ESL design challenges head on.



ESL design started...

ESL design completed.

